

FIG. 1

PRIOR ART

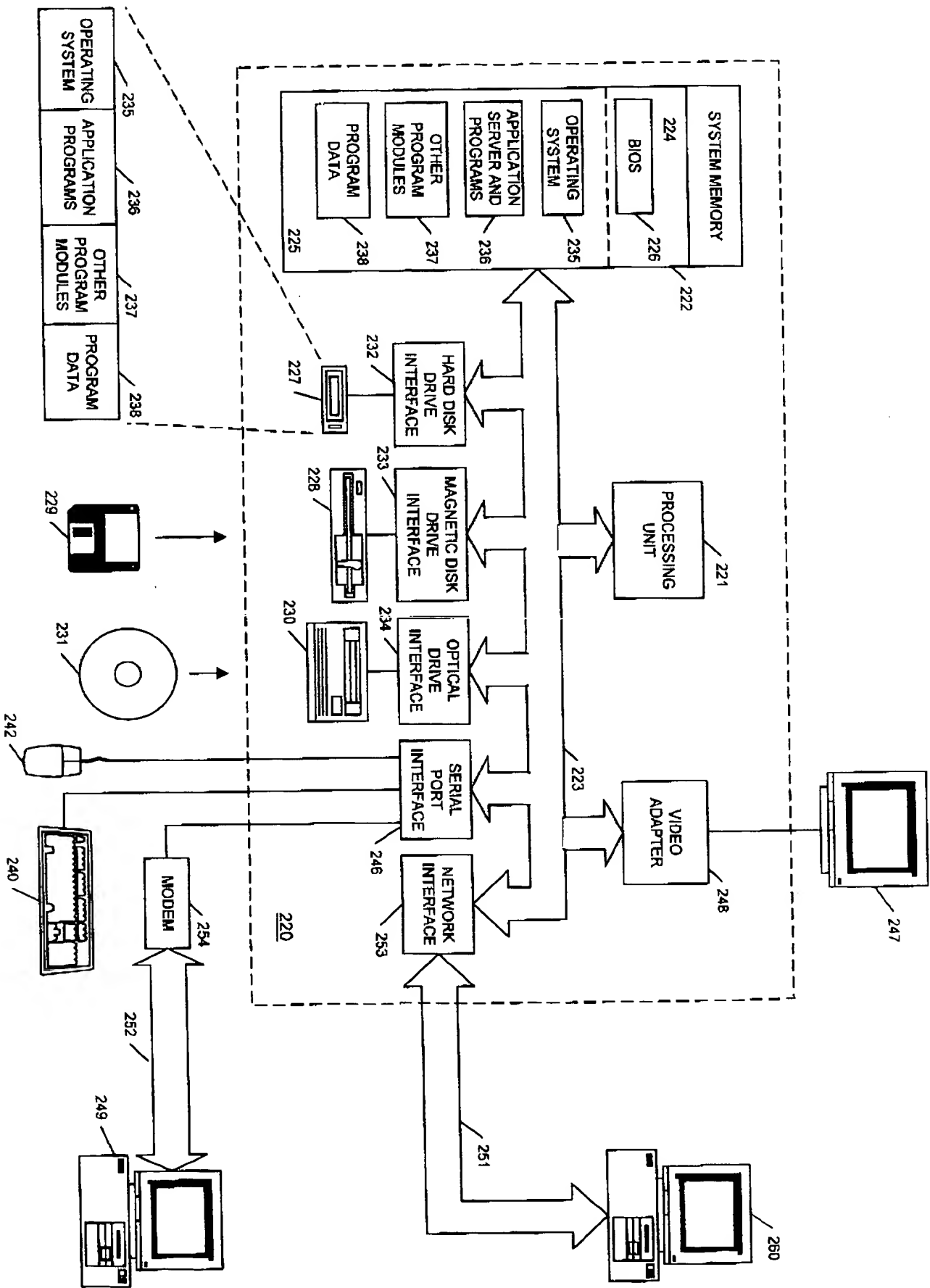


FIG. 2

FIG. 2 is a block diagram of a computer system architecture.

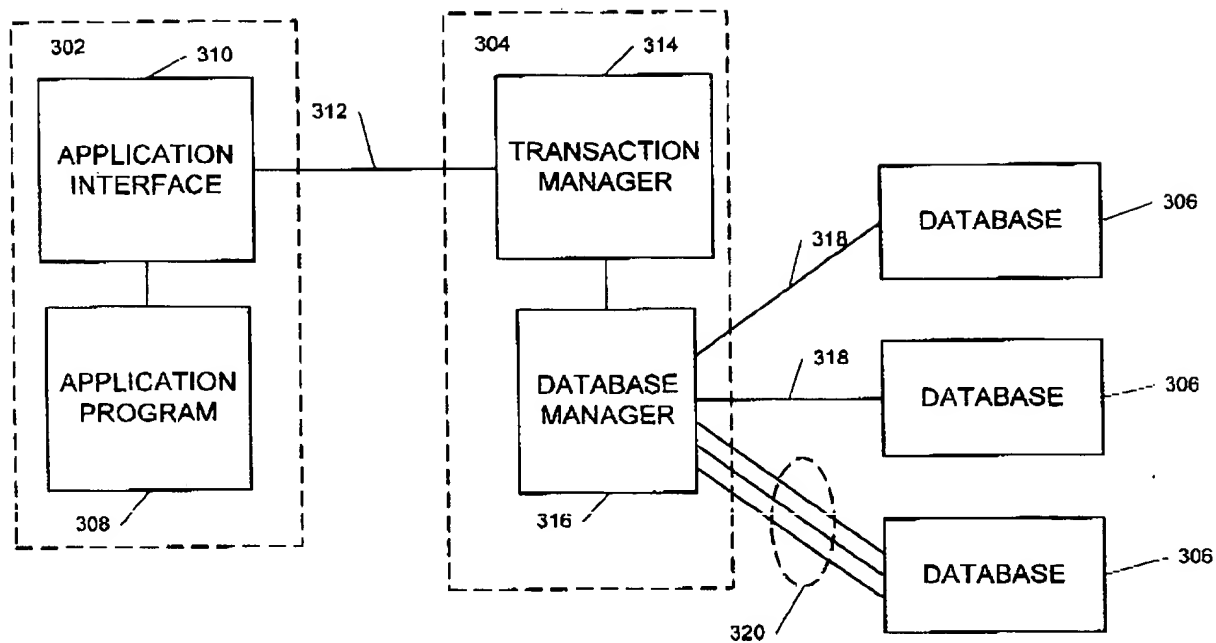


FIG. 3

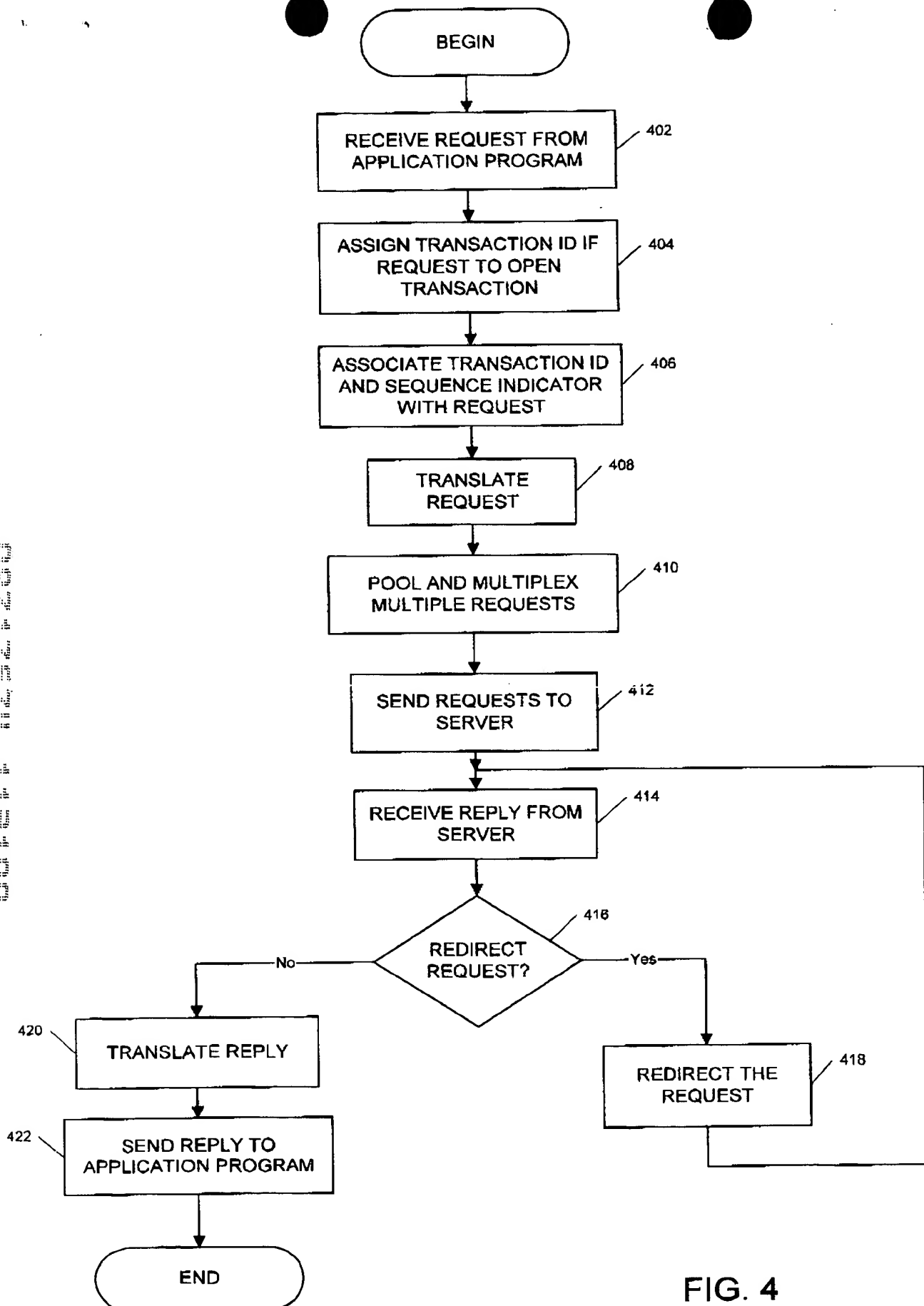
[illegible]

FIG. 4

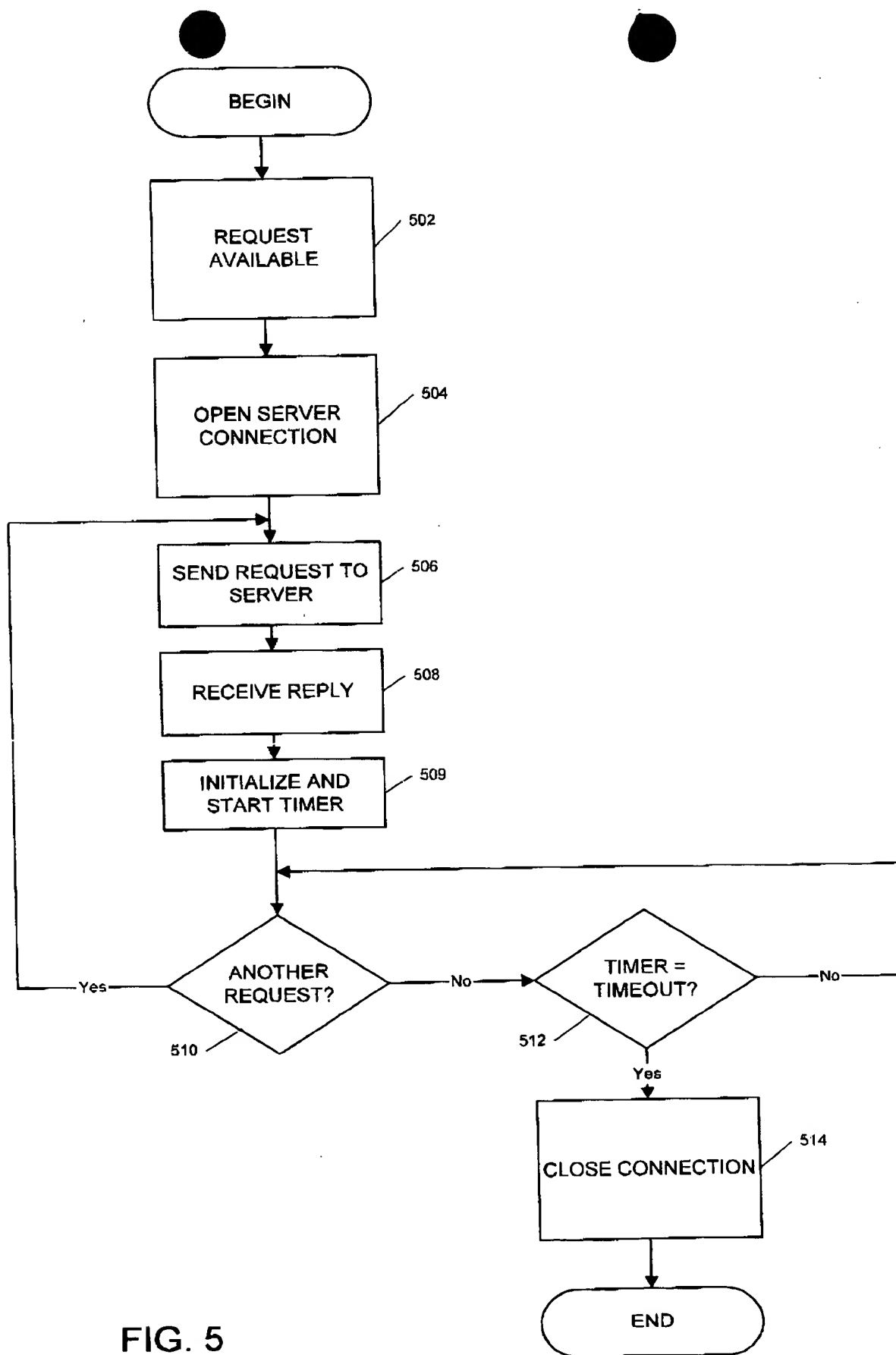


FIG. 5

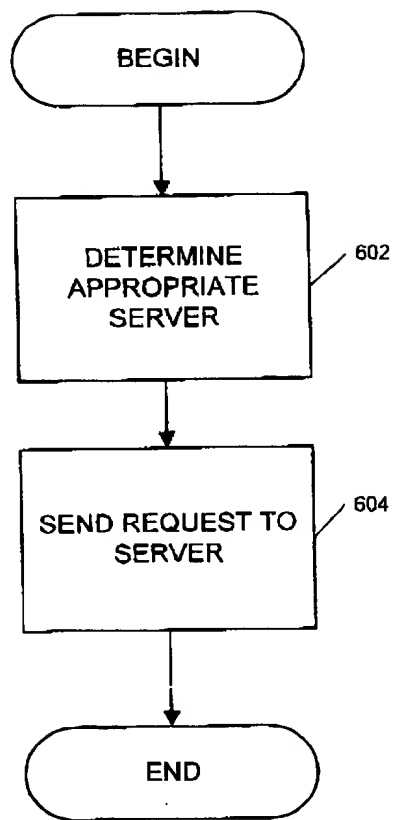


FIG. 6

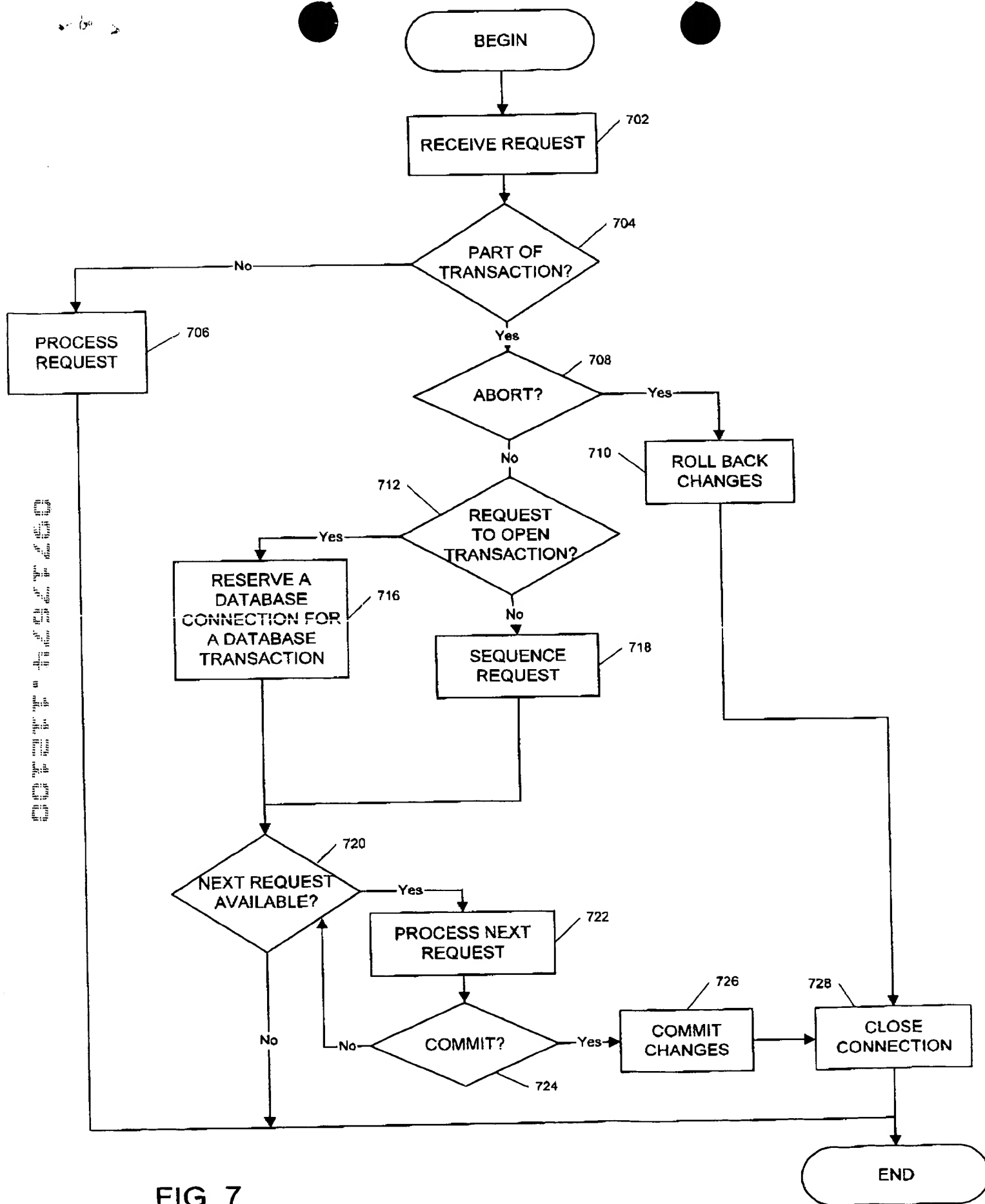


FIG. 7

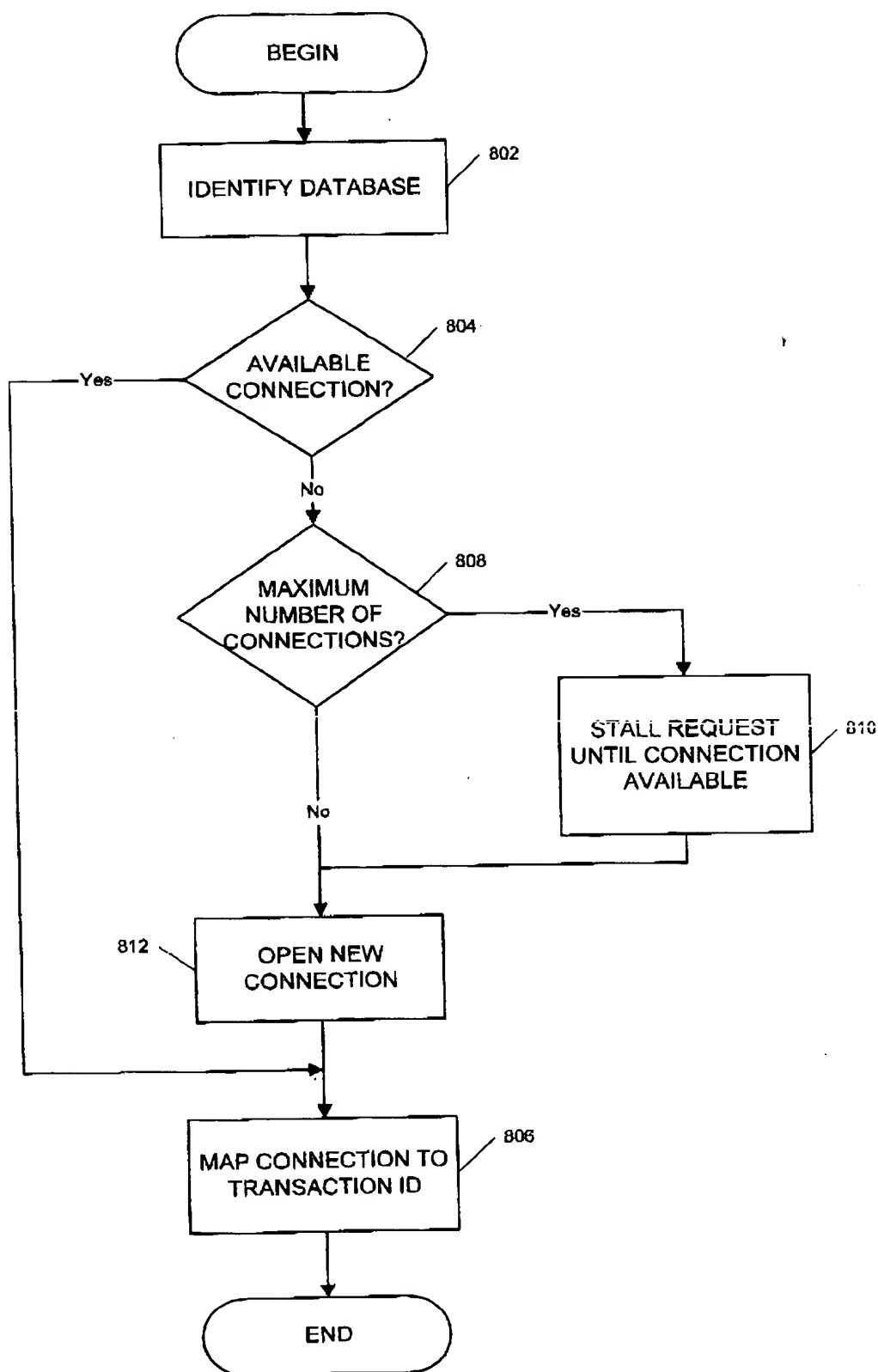


FIG. 8